# Project1 Report

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1. Sequential Gaussian Elimination

Gaussian elimination is composed of two major parts: in the first place, the program will transfer the original matrix into row-echelon form by iterating each row i from 0 to size-1, exchanging rows to move the entry with the first non-zero value to the pivot position, eliminating corresponding rows after row I; and the second part is to solve the equation one by one from the bottom.

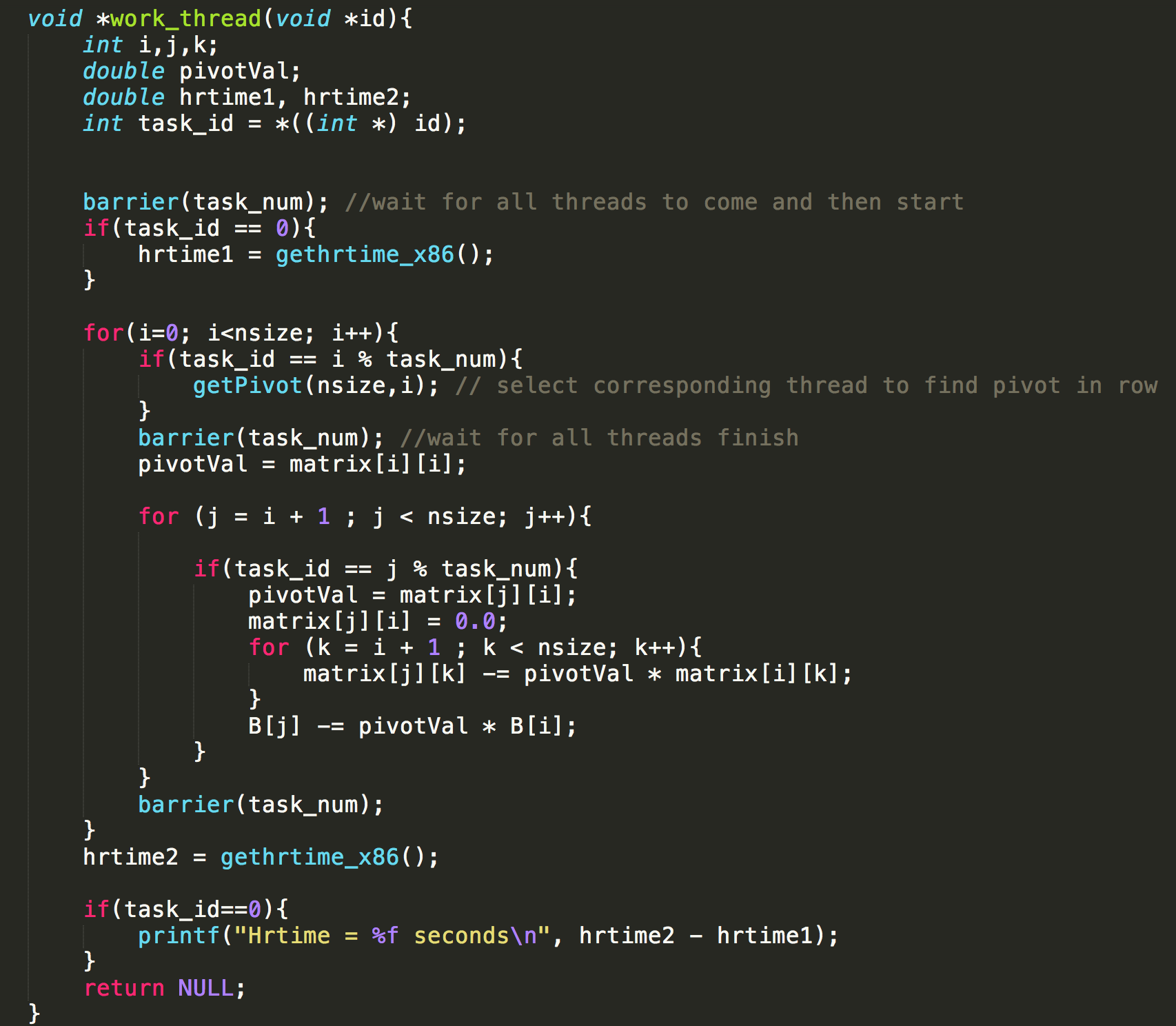
2. Parallel strategies

As discussed in the requirement, the first part of Gaussian elimination is perfect for parallelism and here I have tried two possible solutions to deal with it. The major part of computeGaussian() includes three for loop indexed by i, j and k correspondingly. As we can see, the inner loop j and loop k is completely independent which could be paralleled in either row oriented view or column oriented view.

2.1 Row oriented approach

Through the outer i loop, certain thread will be responsible to handle rows to find its pivot row. And after that, all the other threads will be launched to reduce the portions of sub matrix they control.

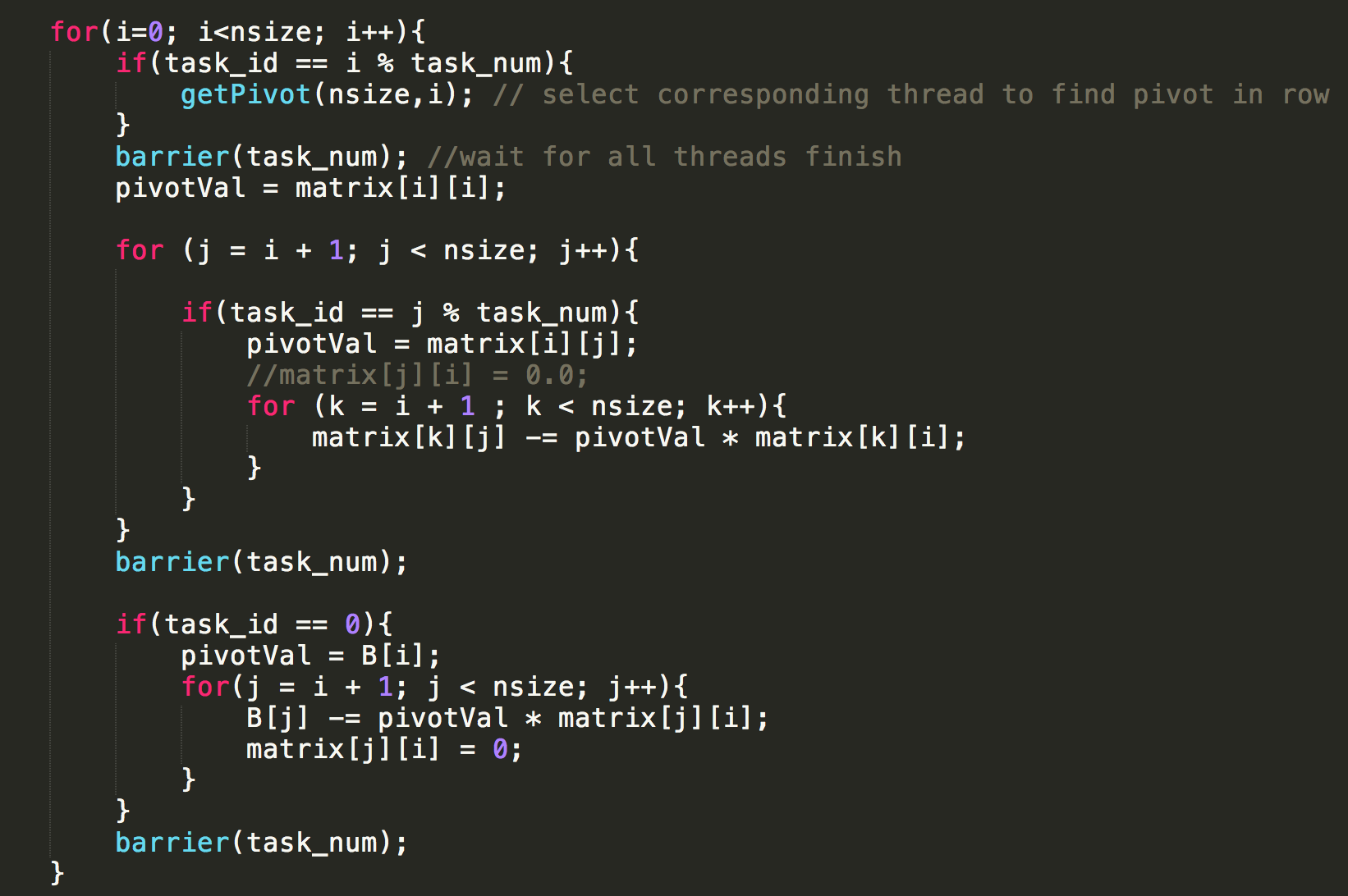
The major subtask function goes like this:



Each thread will be responsible for certain rows and it keeps the character of locality.

2.2 Column oriented approach

On the other hand, instead of eliminating row by row, this procedure could be conducted in a column view:



After finding the pivot row, all threads will be started to partially diminish values below the pivot row.

3. Experimental results

3.1 Environment specification

All of the tests are finished on CSUG nodes cycel2 and cycle3: cycle2 a Dual 6 core CPU with Hyperthreading enabled(that is, the maximum number of logical processors are 24), while cycle3 is which is a Dual 8 core CPU with Hyperthreading enabled(that is, the maximum number of logical processors are 32). Specifically, on Cycle2, the processor Mhz is 2792.994Hz, cache size is 12288kB, and the total memory is 24678888kB; on the other hand, for Cycle3, the processor Mhz is 1200Hz, cache size is 20480kB and total memory is 16382652kB.

To compile the c file:

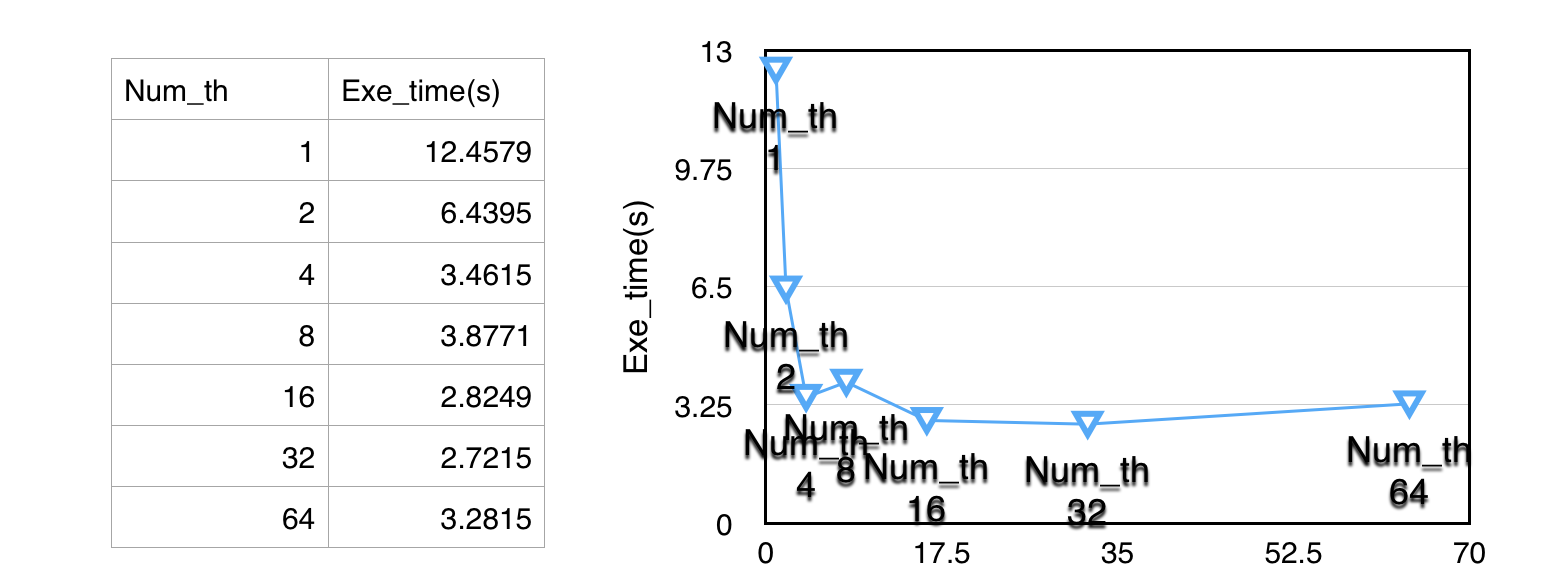
Gcc pth-gauss1.c hrtimer\_x86.c –lpthread

And the input data format is:

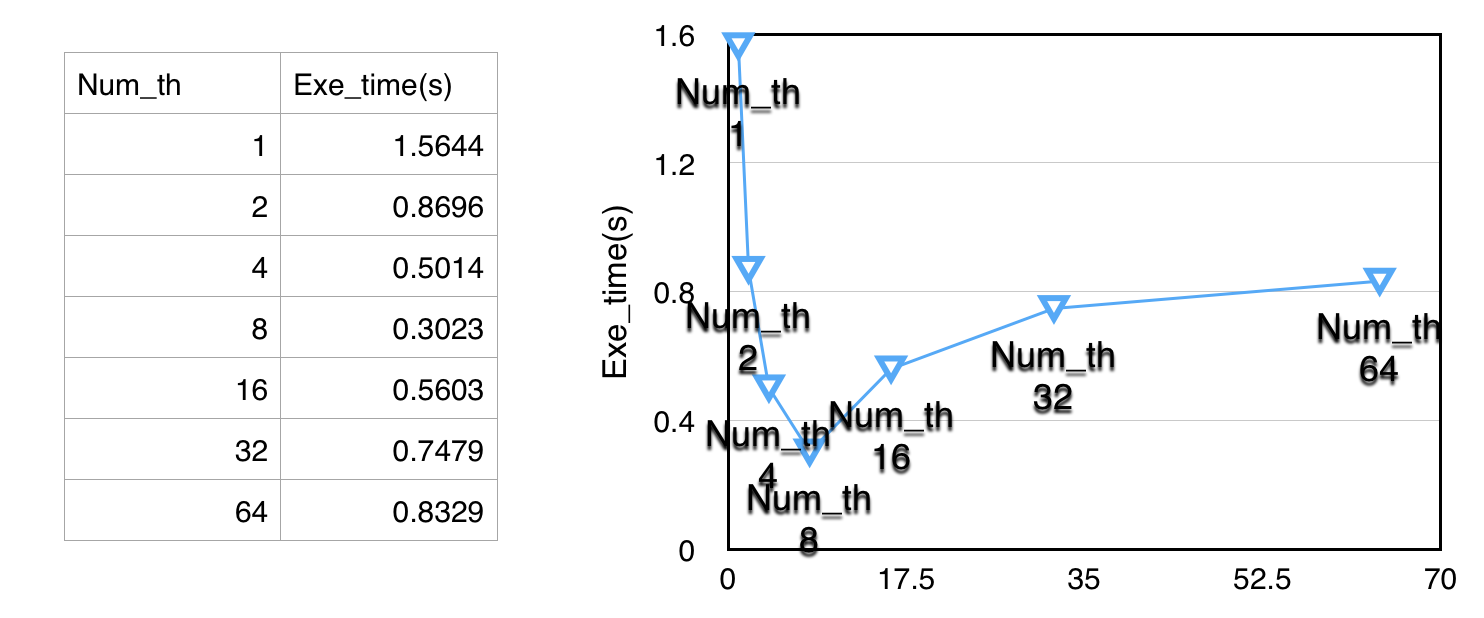
./a.out –s2048 –p32

3.2 Results for Row Oriented Method

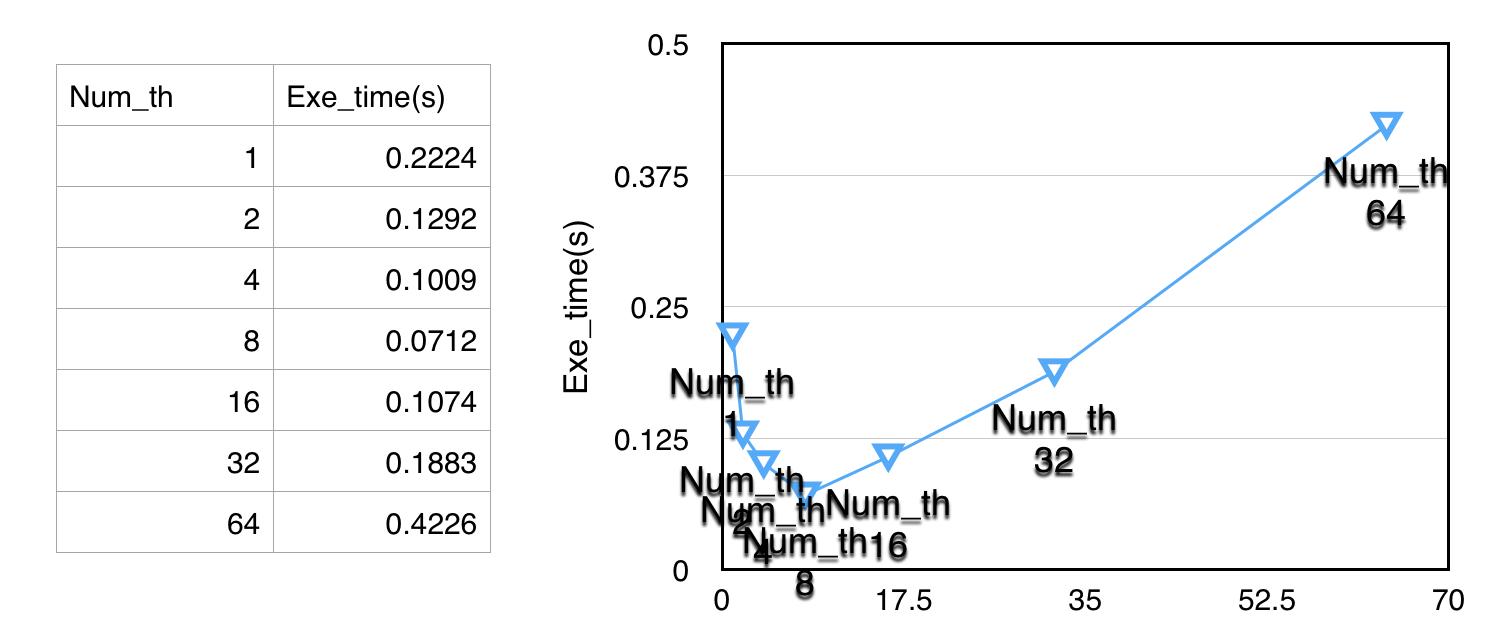
3.2.1 Cycle3

a) s = 2048

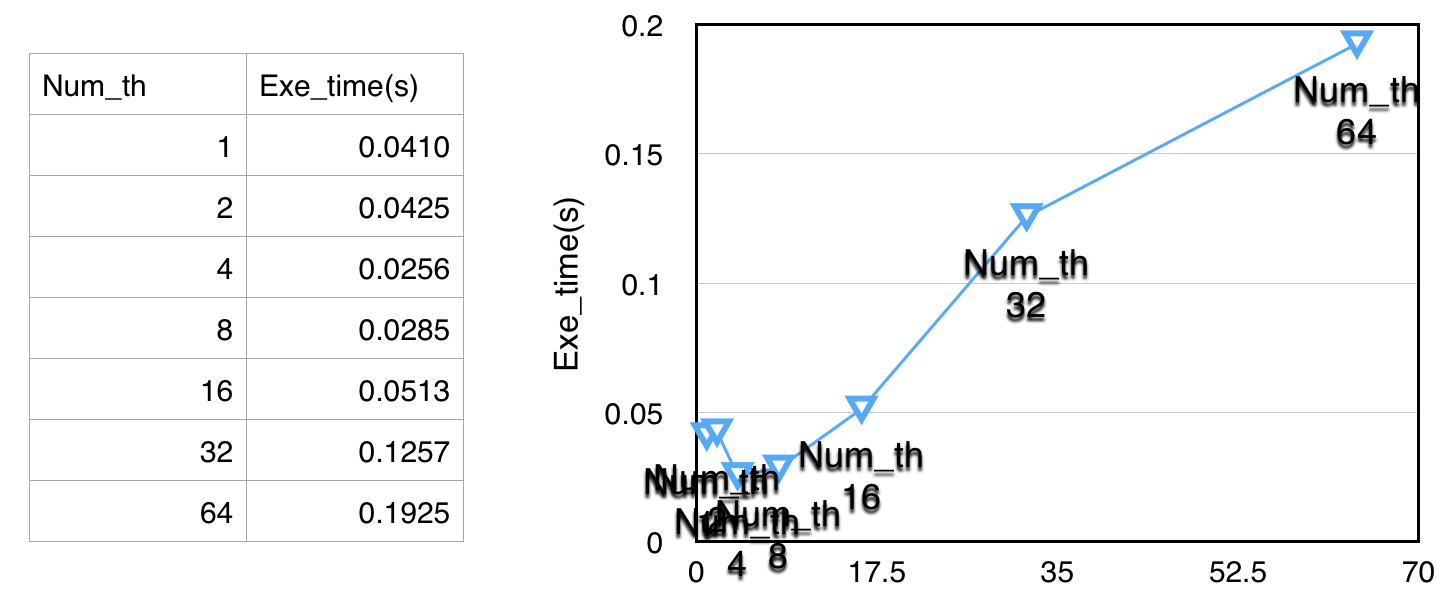
b) s = 1024



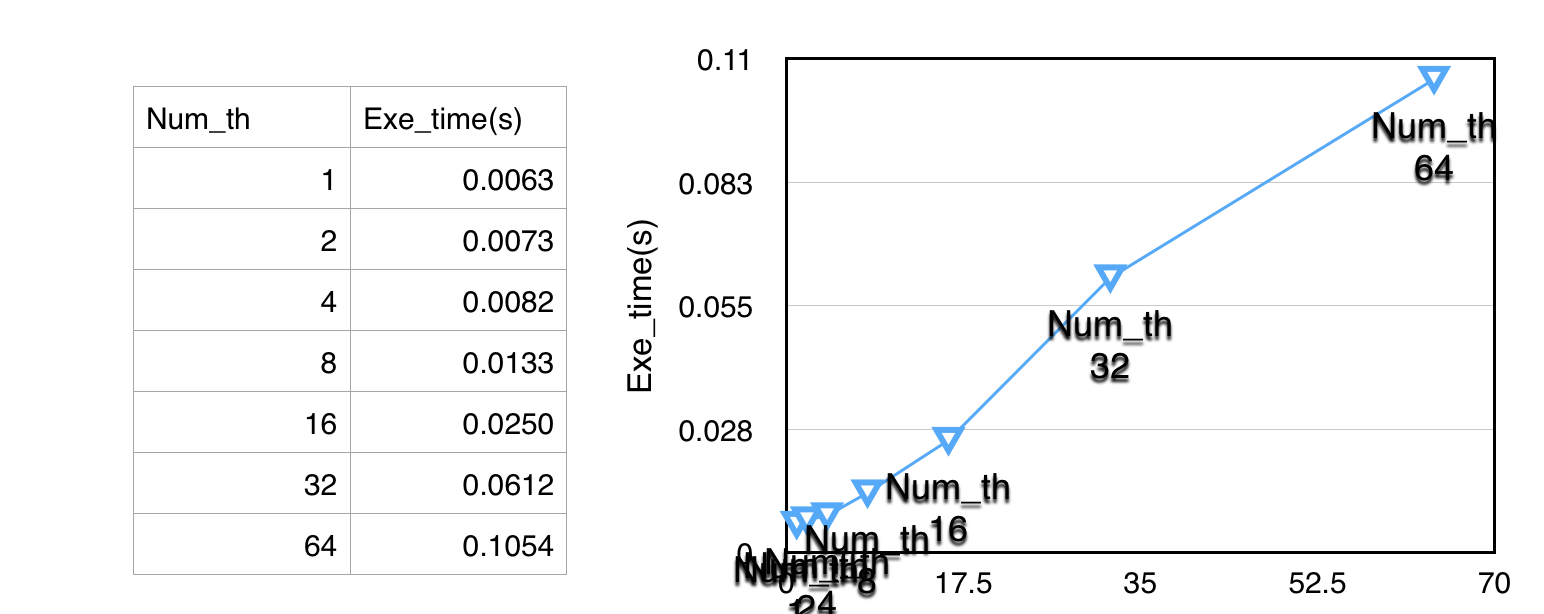
c) s = 512



d) s = 256

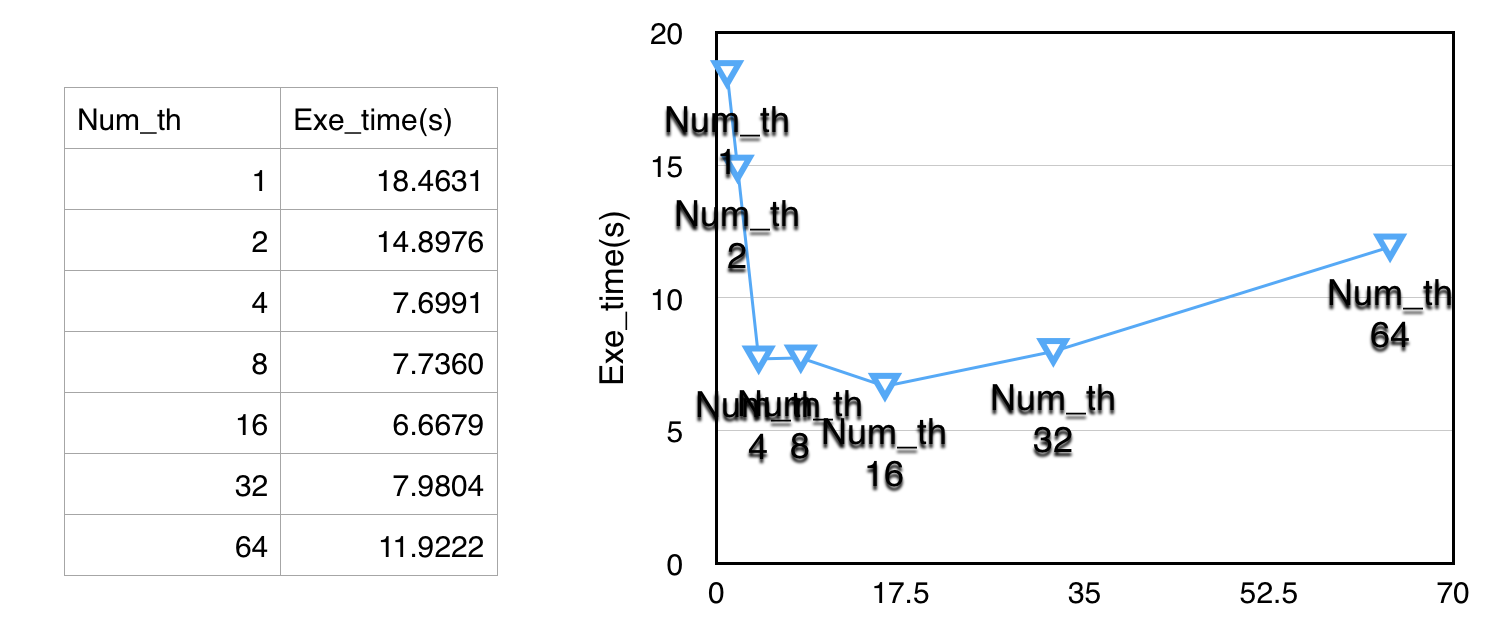


e) s = 128

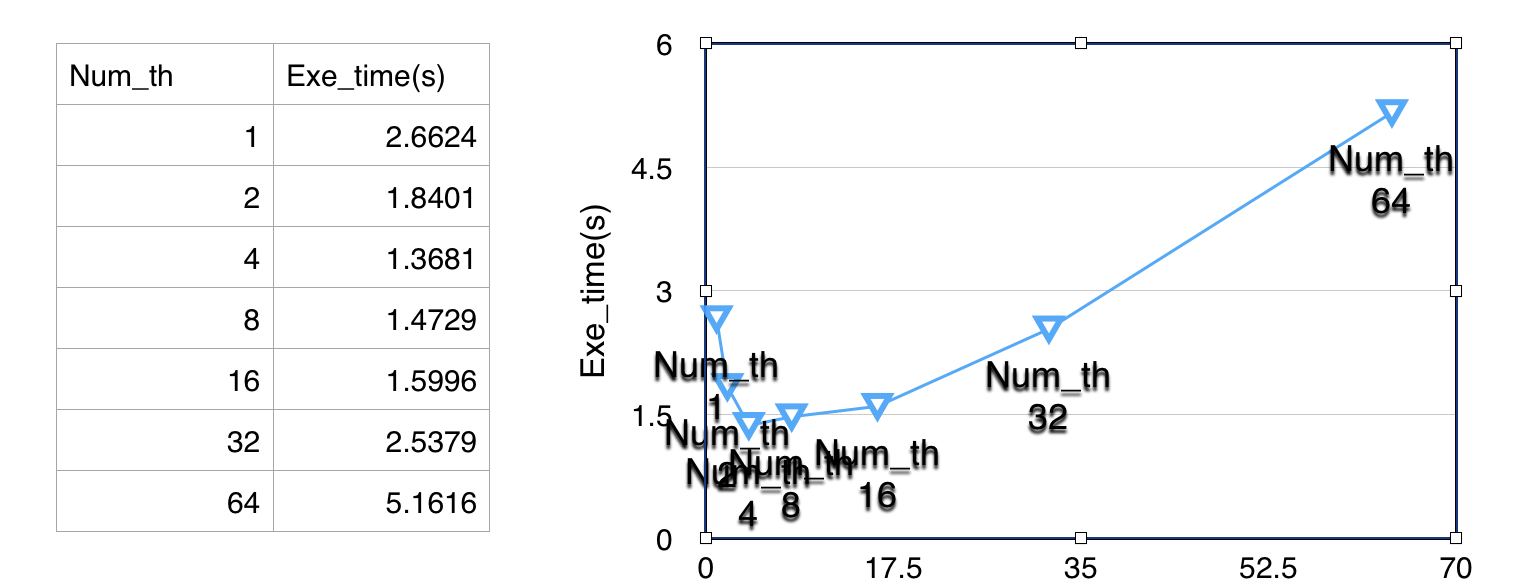


3.2.2 Cycle2

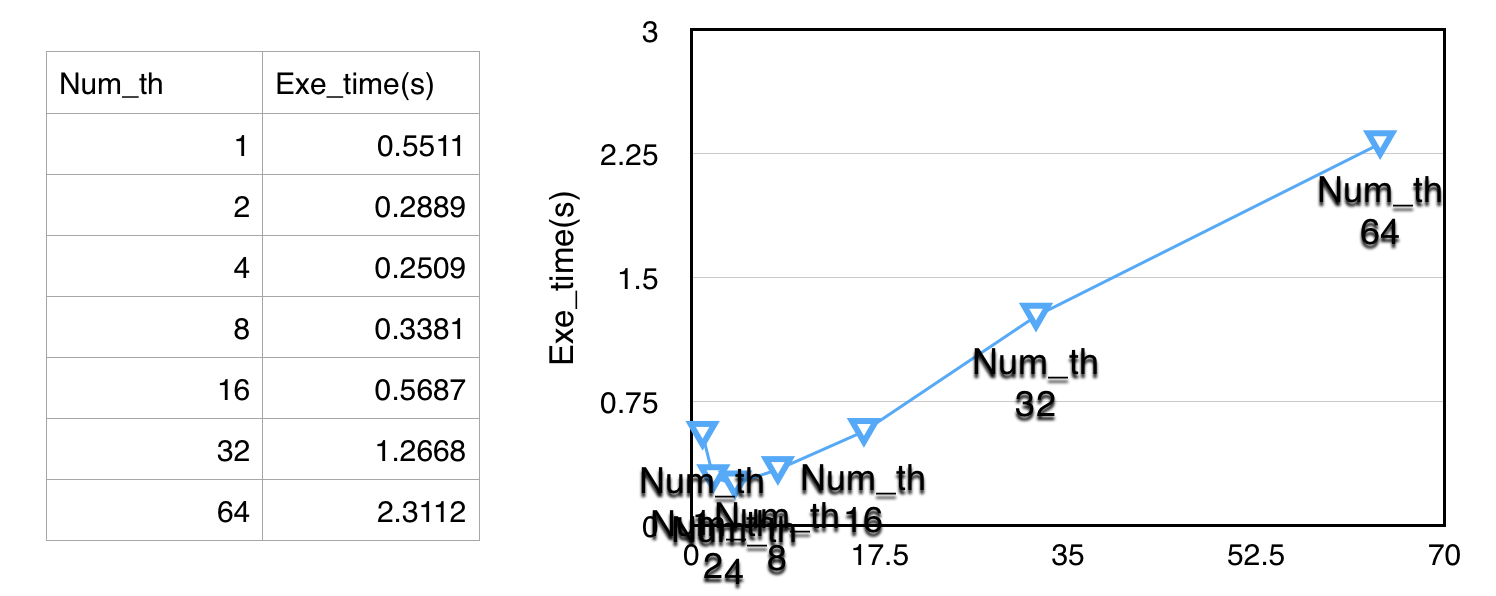
a) s = 2048



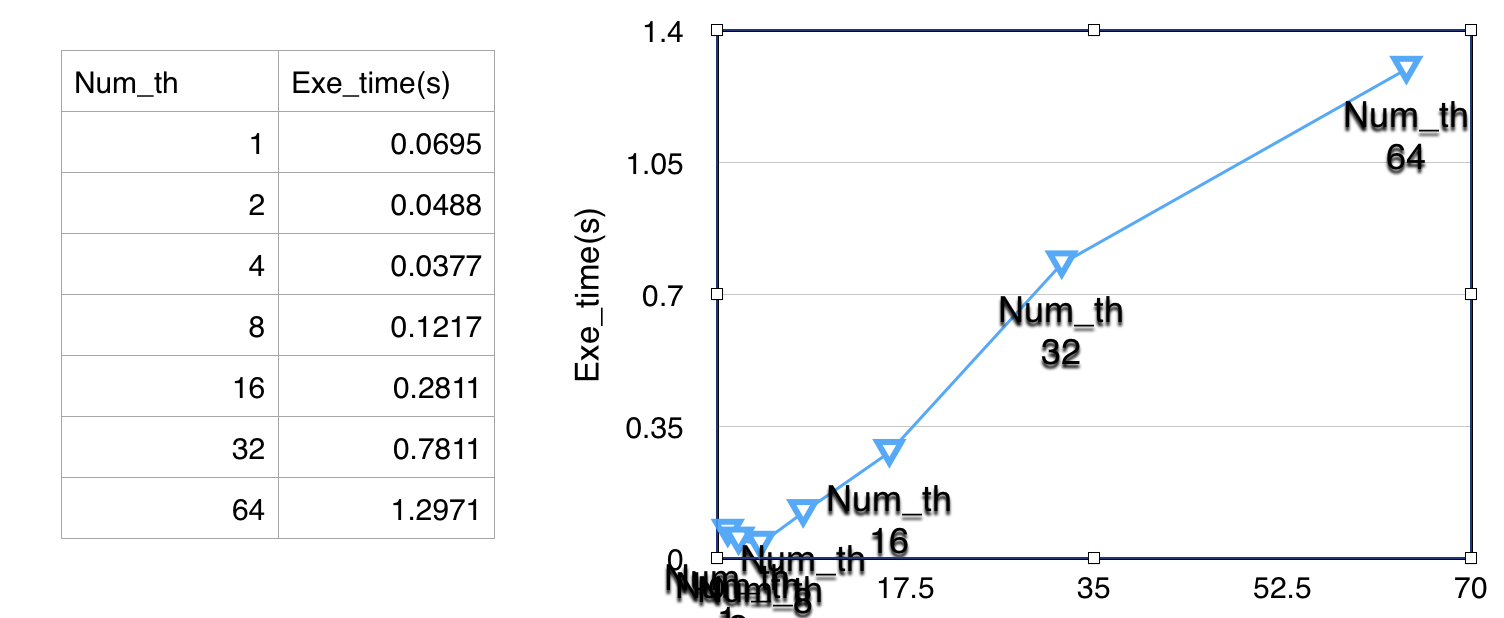
b) s = 1024



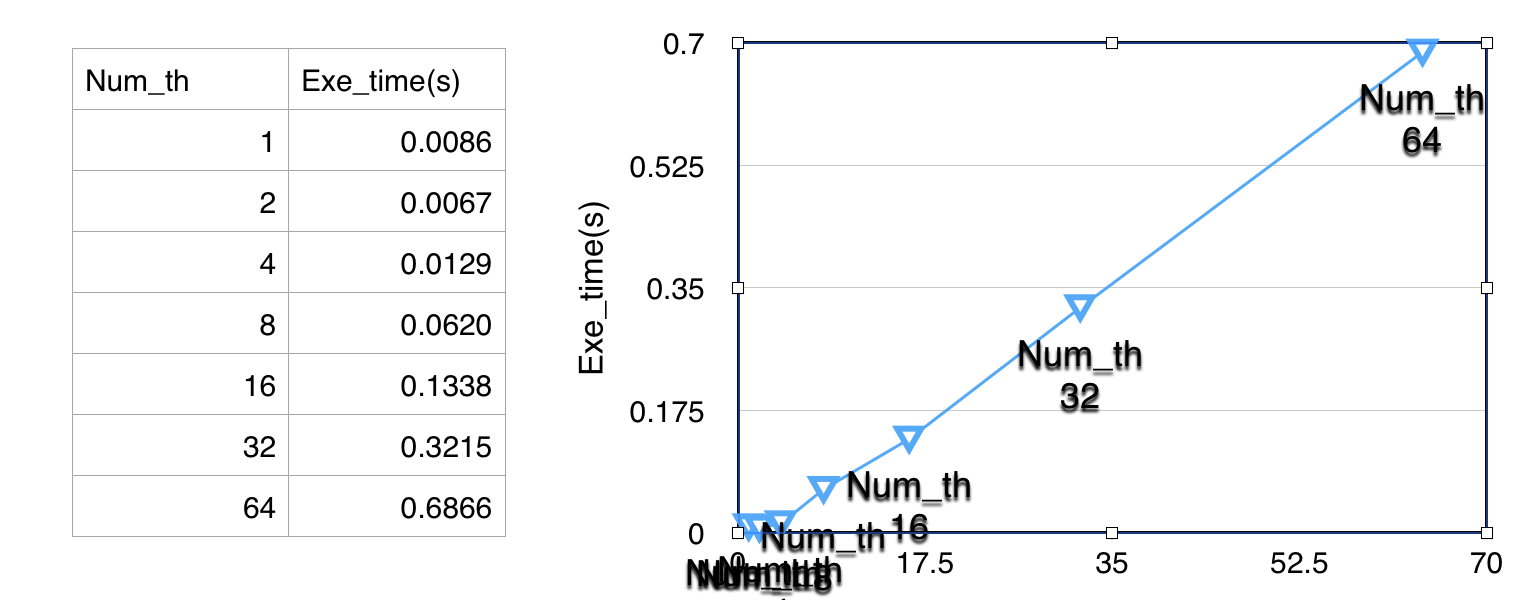
c) s = 512



d) s = 256



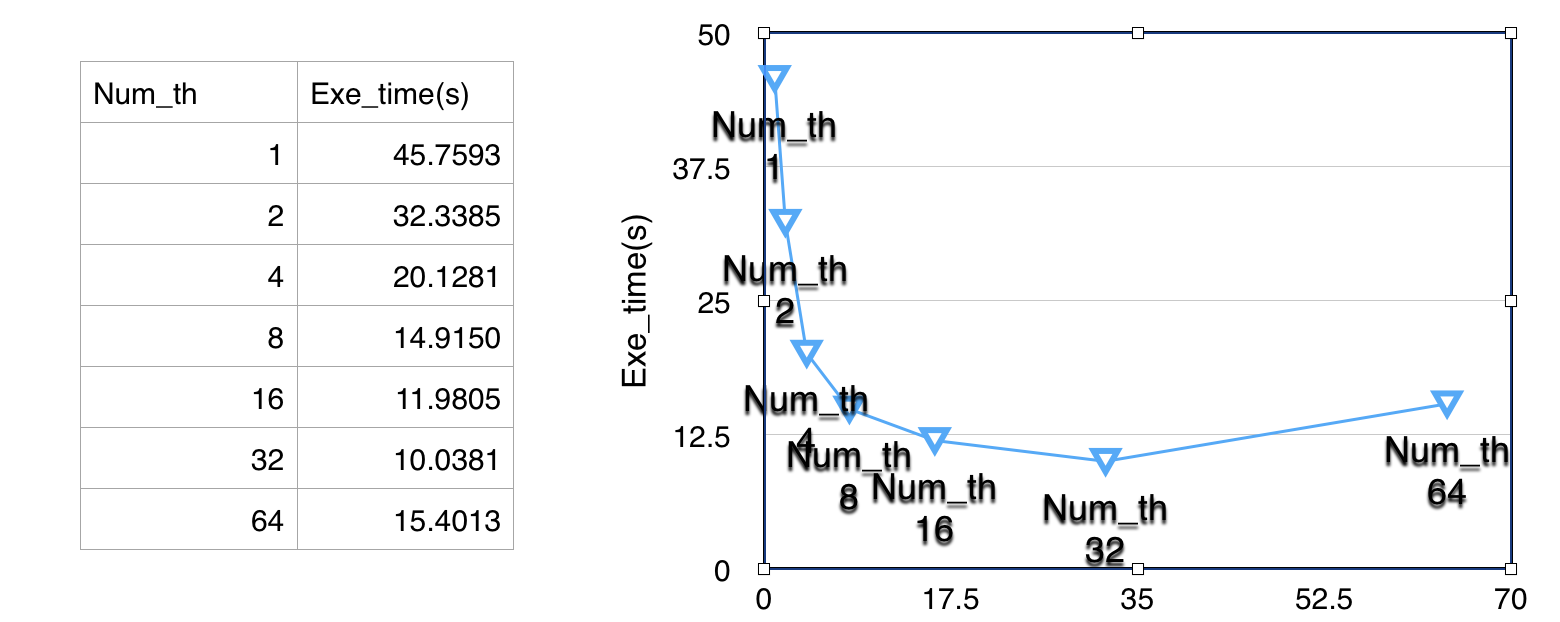
e) s = 128



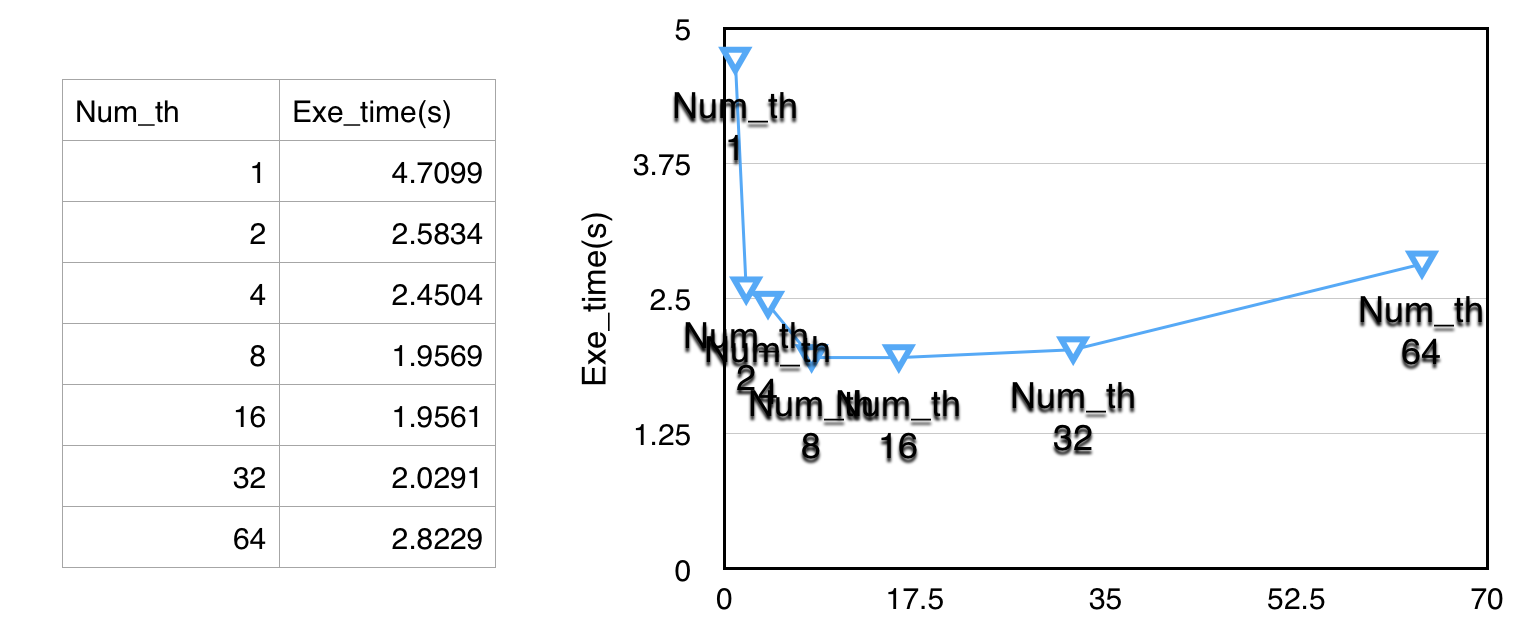
3.3 Results for Column Oriented Method

3.3.1 Cycle3

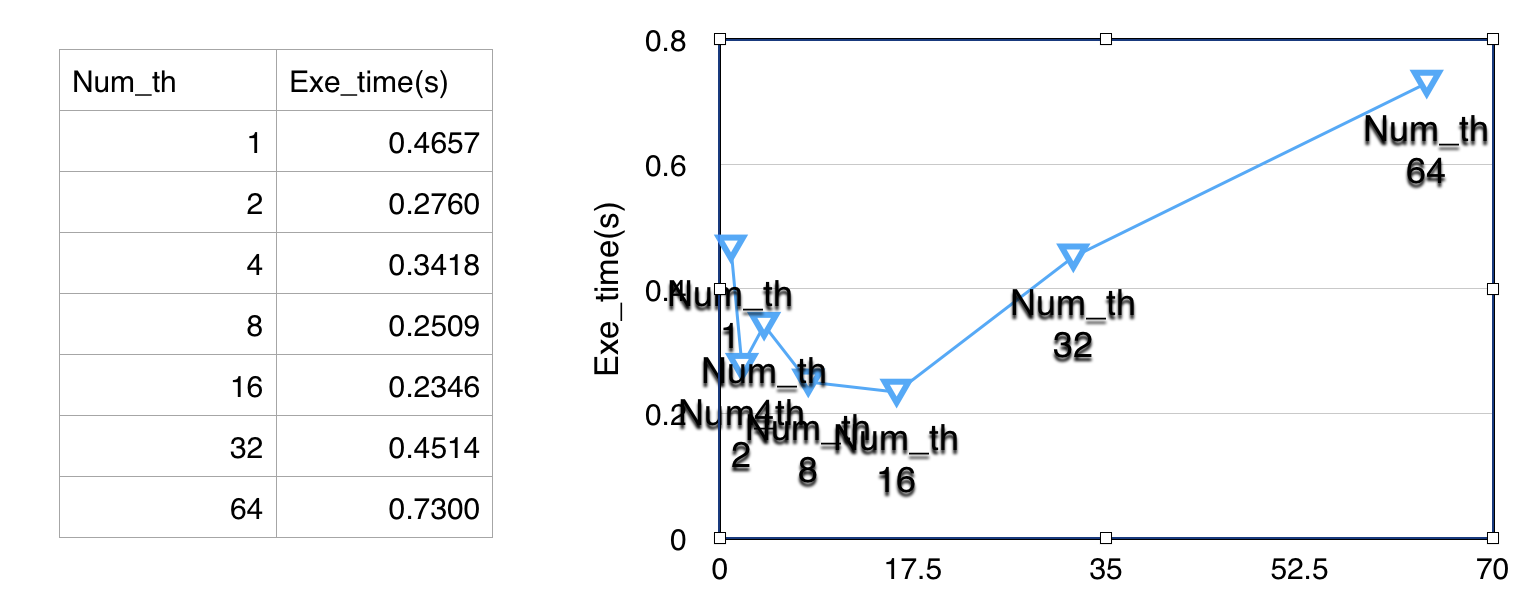
a) s = 2048



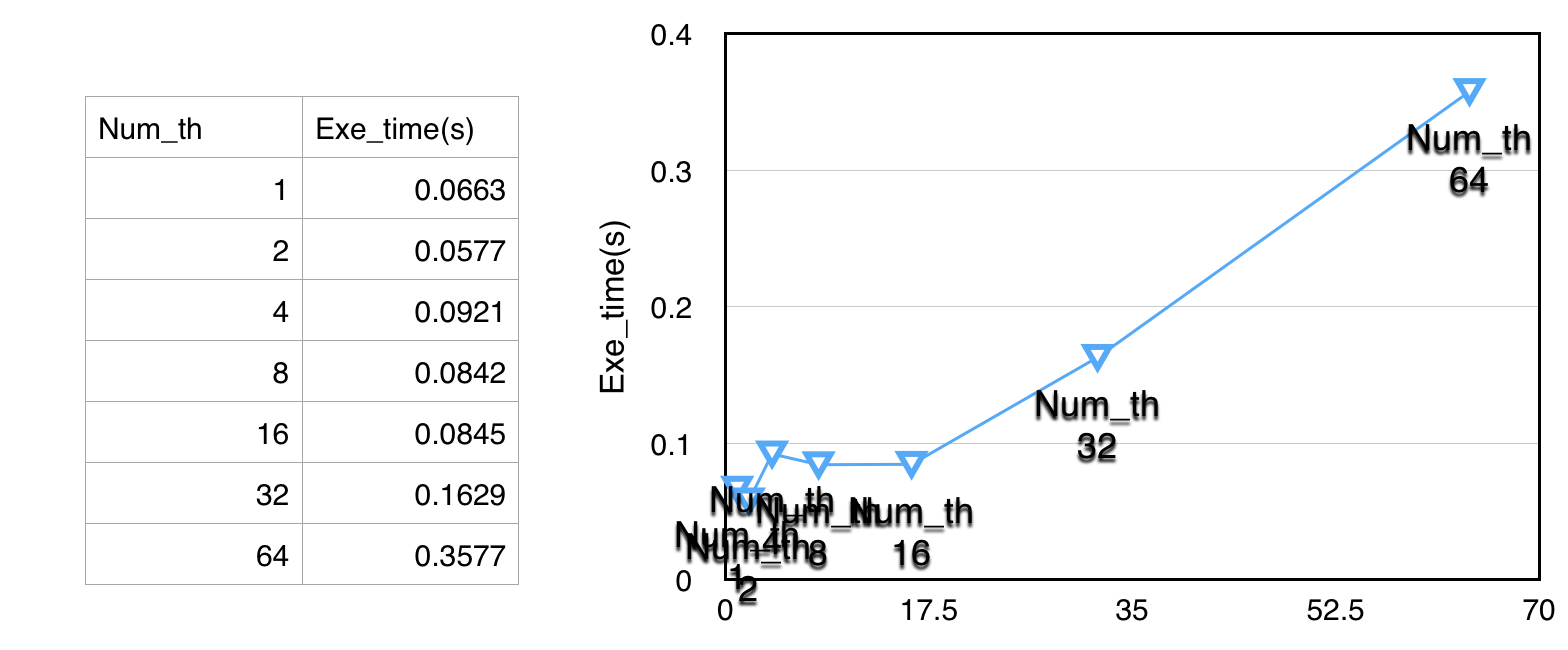
b) s = 1024



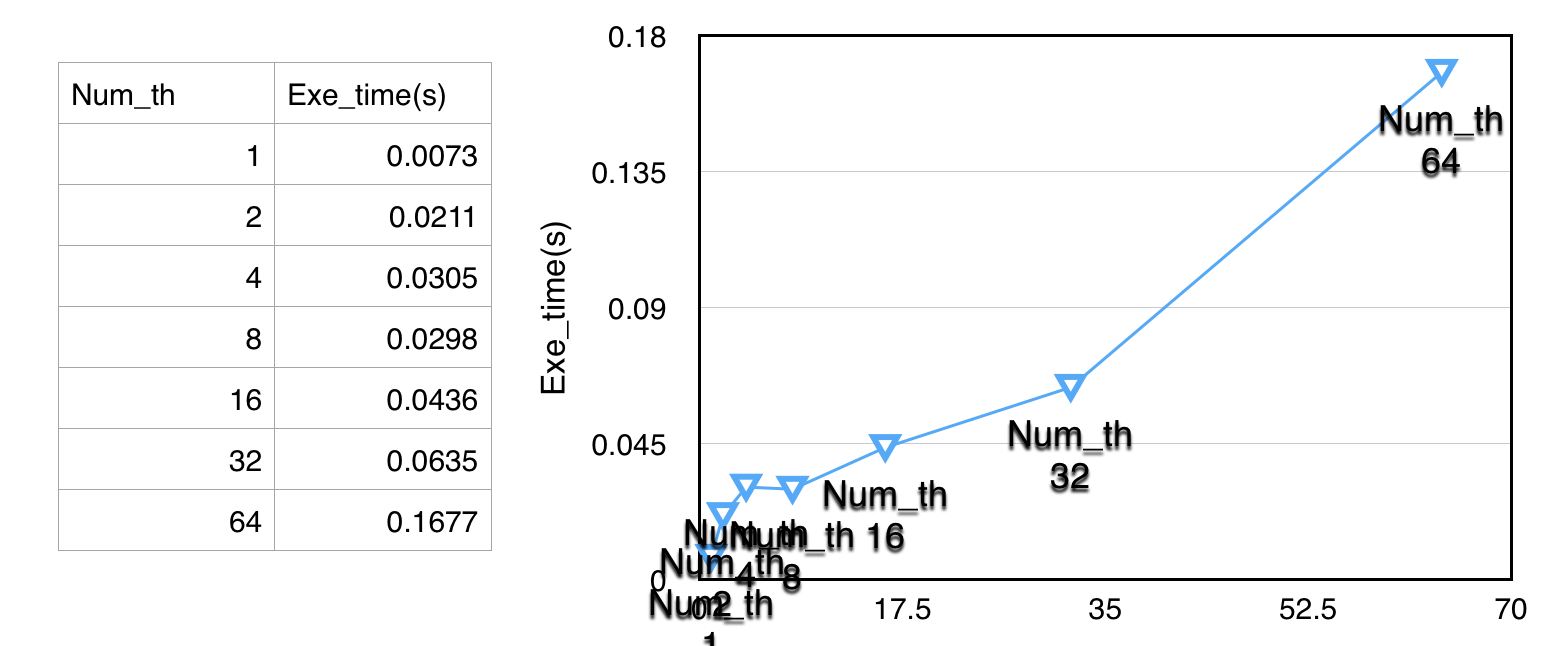
c) s = 512



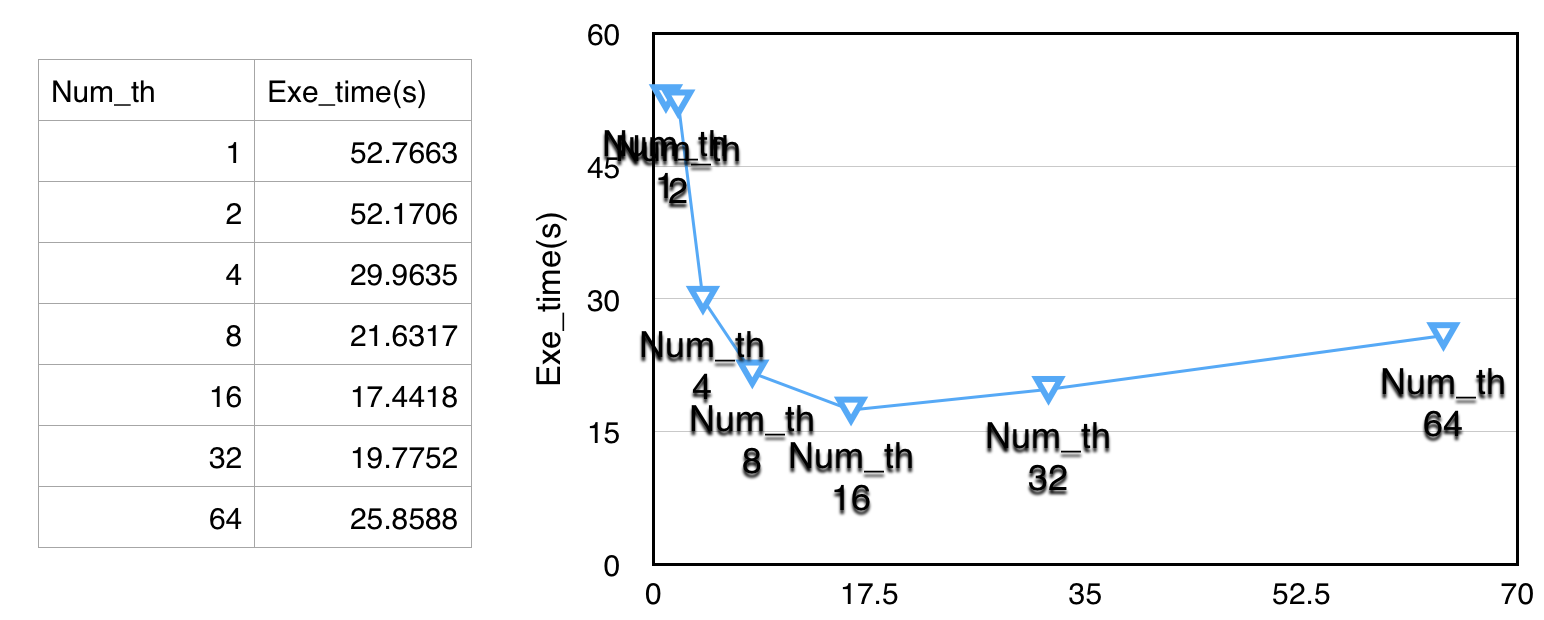
d) s = 256



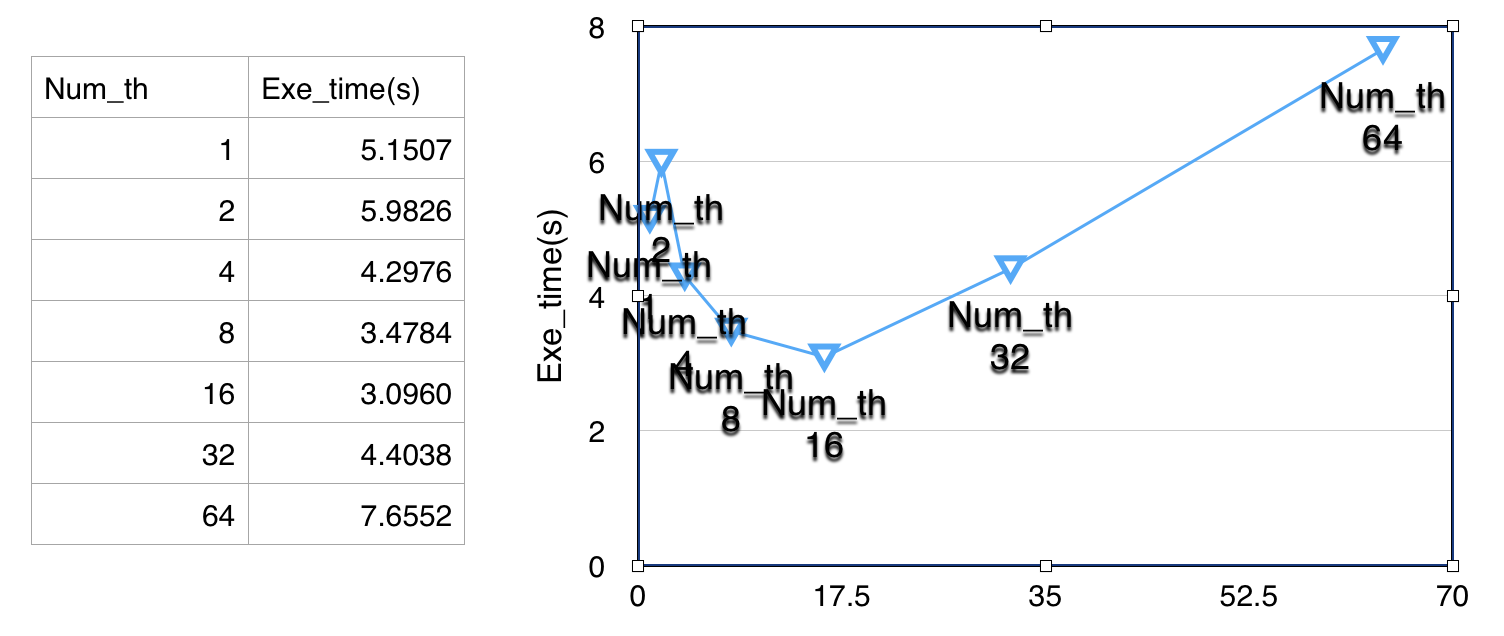
e) s = 128



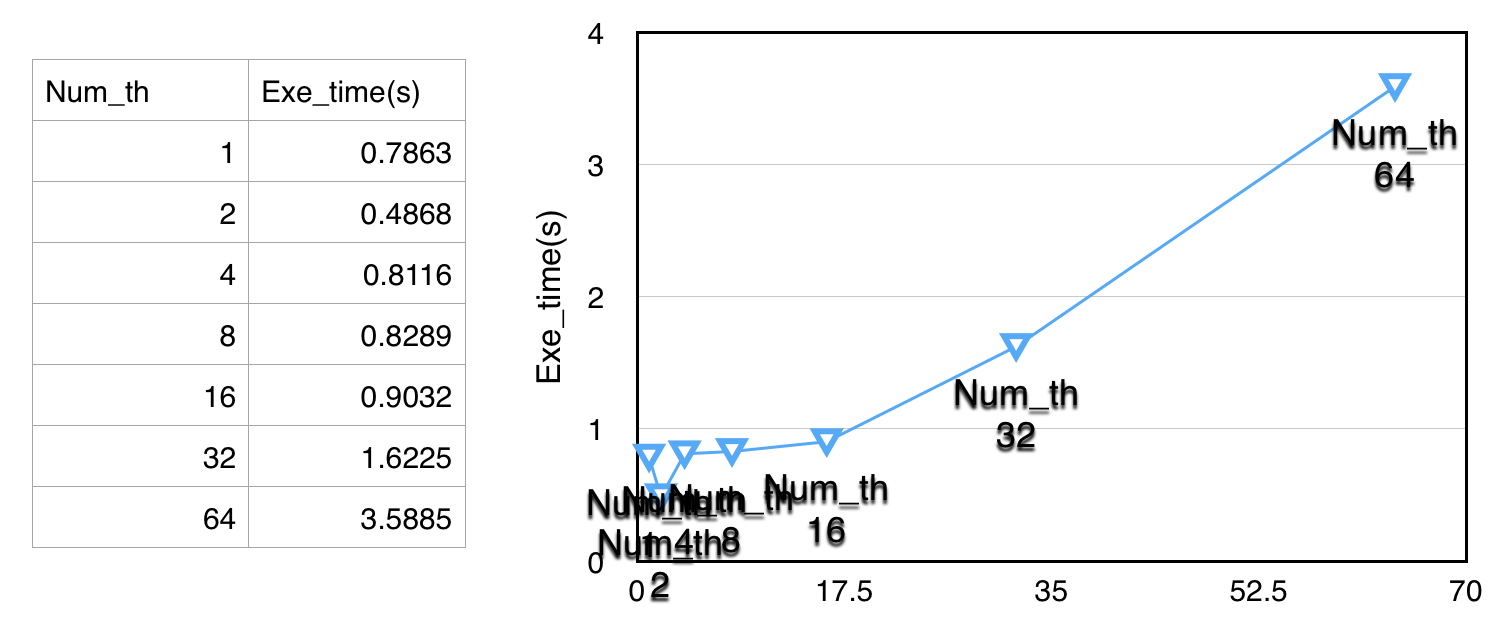
3.3.2 Cycle2

a) s = 2048

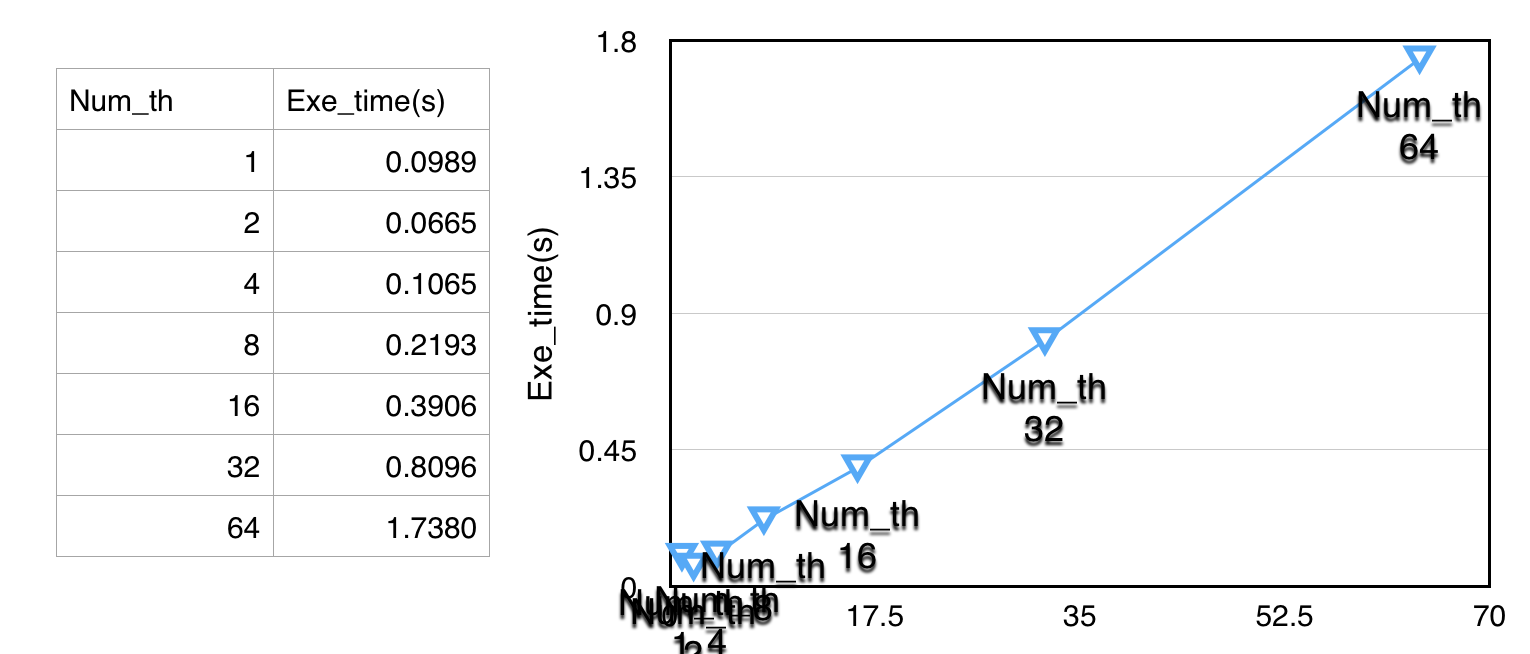
b) s = 1024



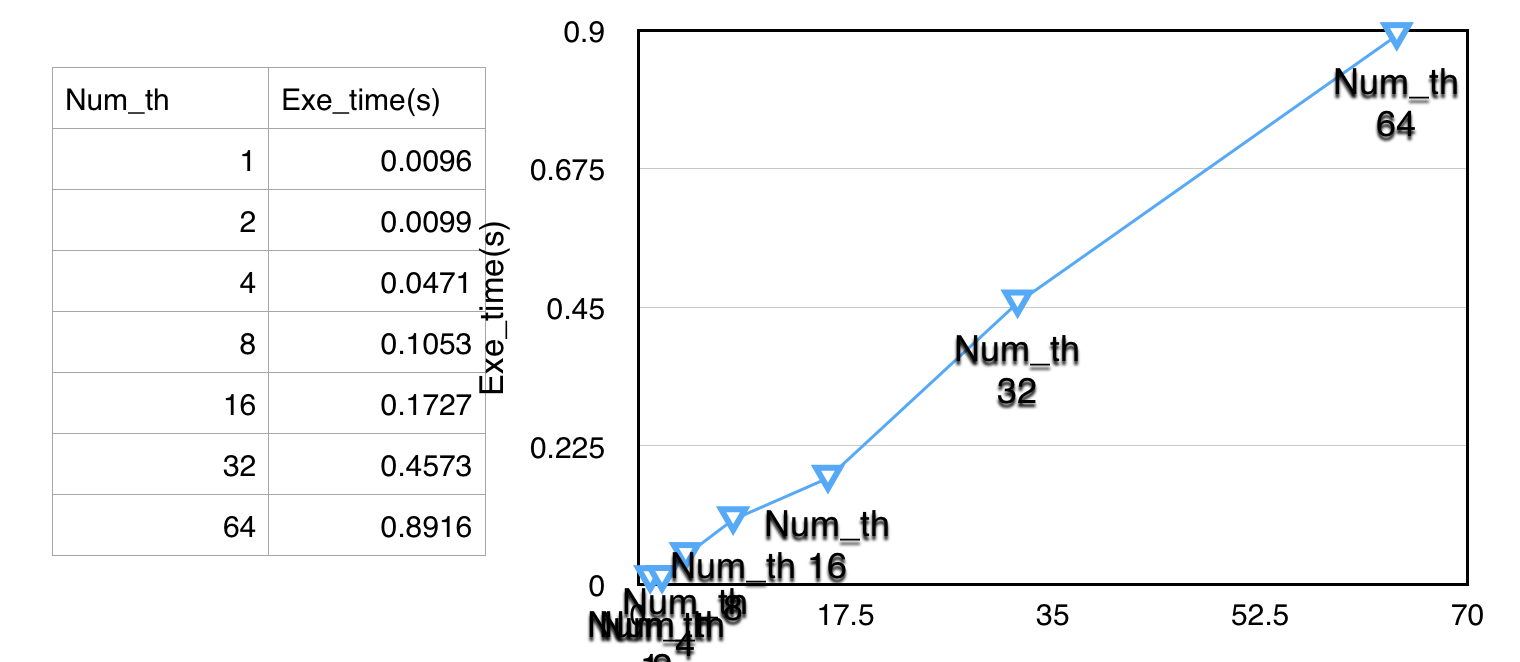
c) s = 512



d) s =256



e) s = 128



3.4 Time analysis

Generally, the execution time is composed by computation time, idle time and communication time.

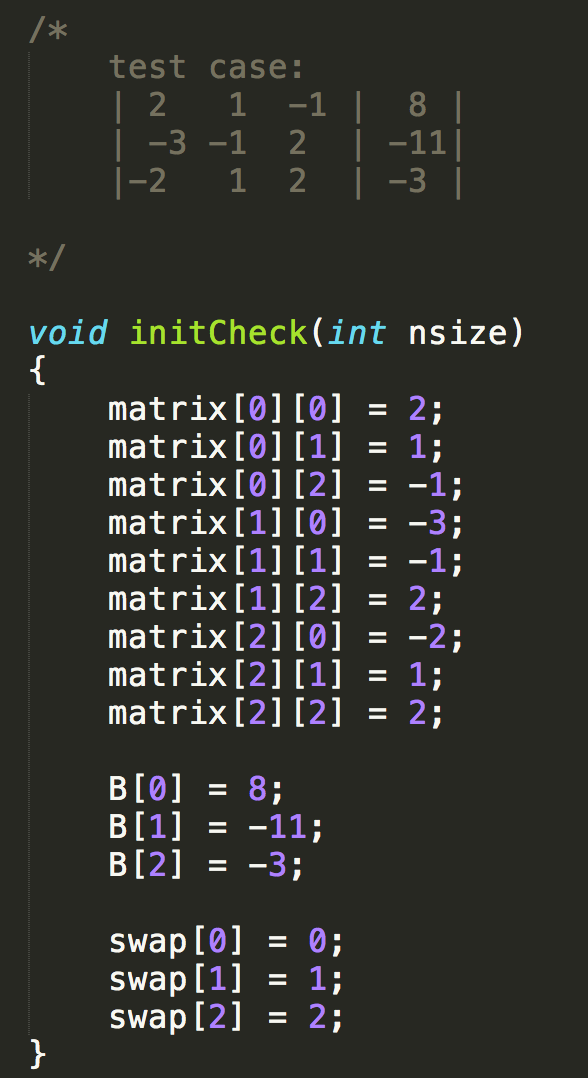
First of all, computation time is the time spent on data computation and it is the part that benefits the most from parallelism. For large size problem, most of time spent on computation; while idle time is the time spent waiting for data from other processor, like I/O for every thread; and finally, communication is the part of time for data transfer between threads. For small size problem, idle time and communication time are the majority part.

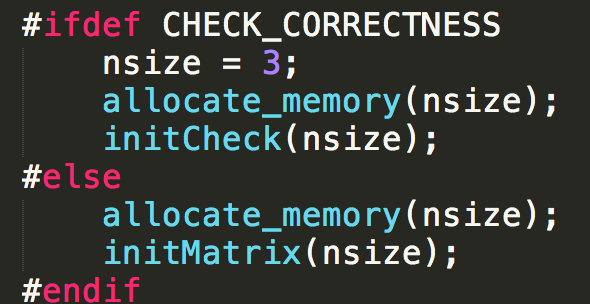
3.5 Conclusion

1. The entire execution time heavily depends on the problem size, i.e. matrix length;
2. Parallelism for small size problem is not always a good idea, since the overheads for threads creation/join and communication count for a large part of time. That is, speed up and efficiency become more ideal when the length of matrix increases;
3. Mostly influenced by the matter of locality, Column Oriented Method suffers a lot from the I/O issues and turns out to be a inferior solution;
4. With large problem size(s=1024 or s=2048), increasing the number of threads always leads to better performance and it reaches the best state when logical threads equals to number of physical processors.

4. Extra test case

It is important to check the correctness other than the static initialization, so I add one test case inside:





And the results are proved to be correct in both methods.